

16~40Vin/5V 16.5A out, Sixteen brick

Contents

Contents	1
Ordering Information	1
Outline Diagram	2
Specification	2
Design Considerations	4
Basic Connection	4
Recommended Layout	4
Input Voltage Range	4
Remote Control	
External Capacitance	5
Output Voltage Adjust	
Remote Sense	
Over Temperature Protection	
Thermal Consideration	
Output Over Voltage Protection	6
Safety Consideration	
Series and Parallel Operation	
ESD Control	
Quality Statement	
Contact Information	





Features

- ◆ Sixteen Brick (36.6mm×26.6mm×12.7mm with case, 33.02mm×22.86mm×10.2mm no case)
- ◆ Input Under Voltage Protection (13 to 15Vdc Turn off)
- ◆ Positive Logic Control (3.5V to 15V or floating Turn on)
- ◆ Output Voltage Adjust Range: ±10% of the rated output voltage
- ◆ Output Short-circuit Protection, automatic recovery
- ♦ High Efficiency up to 91.5% (28V, full load)
- ◆ 1500Vdc Min. Isolation Voltage
- lacktriangle 110 CTyp. Over Temperature Protection
- ◆ Baseplate Temperature :-40 °C to +100 °C
- ◆ Applications: Telecom& Datacom ,Industry, Rail transit and Railway etc.

Ordering Information

See Contents for individual product ordering numbers.

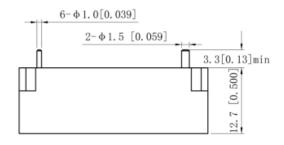
Suffix	Description	Ordering No.
	Positive Logic Control:	ZDS80-28S5
N	Negative Logic Control	ZDS80-28S5N
В	Equipped with metal baseplate. A heatsink can be installed on the baseplate.	ZDS80-28S5B
С	Equipped with plastic case and metal baseplate.	ZDS80-28S5C
NBC	Negative Logic Control. Equipped with metal baseplate and plastic case	ZDS80-28S5NC

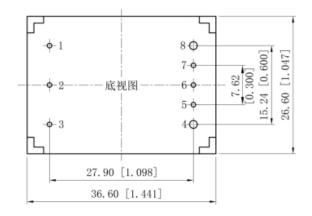
Page 1 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

Outline Diagram





Pin	Symbol	Function				
1	-Vin	Negative Input				
2	CNT	Remote Control Pin				
3	+Vin	Positive Input				
4	+Vo	Positive output				
5	+S	Positive Remote Sense				
6	TRIM	Output voltage adjust				
7	-S	Negative Remote Sense				
8	-Vo Negative Output					
Notes: all dimensions in mm(inches)						

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Tolerances:X.X±0.5mm(X.XX±0.02)

X.XX±0.25mm(X.XXX±0.010)

Specification

Unless otherwise specified, all values are given at: 25° C, one standard atmosphere pressure, pure resistive load and basic connection.

Inp	ut	Symbol	Min	Тур.	Max	Unit	Conditions
Input V	oltage	V_{in}	16	28	40	Vdc	_
Input C	urrent	I_{in}	_	-	5.8	A	V _{in} =16Vdc, I _O =16.5A
Positive Logic	ON	-	3.5	-	15	V	
Remote	OFF	-	-0.7	-	1.5	V	Refer to $-V_{in}$
Control	Current	-	_	-	1.0	mA	_
Start-up De	elay Time	T_{delay}	=	25	ı	ms	_
Under Voltag	e Threshold	$V_{\rm UVLO}$	13	-	15	Vdc	50% load test
Under Voltag Hyste		$\triangle V_{UVLO}$	_	1.0		Vdc	_
Quiescent In	put Current	_	_	150	200	mA	Io=0A

Output	Symbol	Min	Тур.	Max	Unit	Conditions
Output Voltage	Vo	4.95	5.00	5.05	Vdc	V _{intyp} , Io,nom
Output Current	$I_{O,nom}$	-	16.5	-	A	_
Output Voltage Adjust Range	V_{trim}	4.5	ı	5.5	Vdc	Io≤16.5A P <u>\$</u> 82.5W
Line Regulation	S_{V}	-	-	±0.2	% V _O	V_{in} : 16 \sim 40Vdc, I_{O} =16.5A
Load Regulation	S_{I}	_	_	±0.5	% V _O	$V_{in}=V_{intyp}$, $I_O: 0A \sim 16.5A$

Continue

Page 2 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

Output	Symbol	Min	Тур.	Max	Unit	Conditions
Output Over Voltage Protection Set Point	$V_{\text{ov,set}}$	6.0	ı	7.0	Vdc	$V_{in} = V_{intyp}$
Output Over Current Protection Range	$I_{O,lim}$	18	1	28.1	A	$V_{in} = V_{intyp}$
Output Short-circuit Protection		autoi	natic recov		$V_{in} = V_{intyp}$	
Peak to Peak Ripple and Noise	$\triangle V_{pp}$	ı	ı	50	mV	V _{in} =V _{intyp} , Io,nom, 20MHz bandwidth, a 22μF ceramic capacitor applied at output
Rise Time	T_{rise}	I	10	_	ms	$V_{in} = V_{intyp}$, $I_{O,nom}$, pure resistive load
Output Overshoot	V_{TO}	0	ı	0.5	Vdc	$V_{in}=V_{intyp},I_{O,nom}$, pure resistiveload
Capacitive Load	Co	0	ı	10000	μF	pure resistive load
Remote Sense Compensation Range	V _{sense}	0	_	0.5	V	+S and -S twisted Pair, length is less than 20cm

Ger	neral	Symbol	Min	Тур.	Max	Unit	Conditions	
Load	Voltage Deviation	$\triangle V_{tr}$	_	±250	_	mV	25%~50%~25% Io,nom or	
Transient	Recovery Time	t _{tr}	_	200	_	μs	50%~75%~50% Io,nom; 0.1A/μs	
Effic	iency	η	90	91.5	-	%	V _{in} =V _{intyp} , Io,nom	
Switching	Frequency	f_s	-	400	-	kHz	_	
Isolation 1	Resistance	R _{iso}	100	-	-	ΜΩ	_	
			1500	_	_	Vdc	Input to output Leak Current: 1mA	
Isolation	Isolation Voltage	V_{iso}	1500	_	_	Vdc	Input to case Leak Current: 1mA	
			500	_	_	Vdc	Output to case Leak Current: 1mA	
MT	ΓBF	_	_	2×10 ⁶	_	h	BELLCORE TR-332,	
_	g Ambient erature	_	-40	_	+85	$^{\circ}\!\mathbb{C}$	See the derating curve	
Storage Te	emperature	_	-55	_	+125	$^{\circ}$ C	_	
_	erature ficient	S_{T}		_	±0.02	%/°C	_	
Relative	Humidity	_	10		90	%	No condensing, 40°C±2°C	
Protection	nperature Reference oint	$T_{ m ref}$	95	110	115	$^{\circ}$	See Over Temperature Protection consideration	
	nperature Hysteresis	$\triangle T_{ref}$	_	10		$^{\circ}$ C	Consideration	
Hand S	oldering	Maximum soldering Temperature $< 425 ^{\circ}\text{C}$, and duration $< 5\text{s}$						
Wave S	oldering	Maximum soldering Temperature $< 255^{\circ}\text{C}$, and duration $< 10\text{s}$						

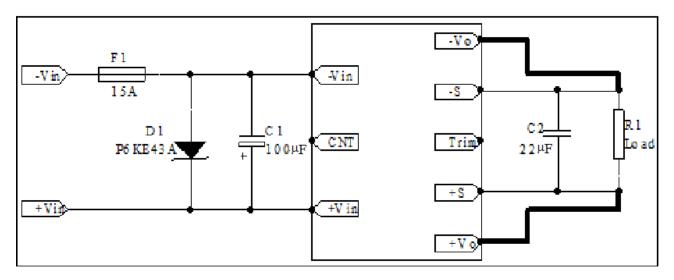
Page 3 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

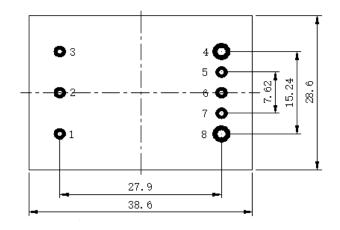
Design Considerations

Basic Connection



Notes: The basic connection indicates the basic requirements that the power module can provide rated output voltage and rated power only. Please refer the instruction followed for further information.

Recommended Layout



NO.	Recommendation & Notes						
Pad	Pad holes: 1.3mm, pad diameter						
Design	including hole:3.0-3.3mm;						
	Isolated Converters, care to the spacing						
Safety	between input and output, input and						
Salety	protective ground, output and						
	protective ground.						
	The Vin(-) and Vo(-) planes should be						
Electri	placed under of the converter						
cal	separately. Avoid routing sensitive						
Cai	signal or high disturbance AC signal						
	under the converter.						

Input Voltage Range

The input voltage range of the DC/DC converter is 16V to $40V_{\circ}$. The input impedance of the converter looks like a negative resistor, which can interact with the reactance of the power bus (including any filter elements that have been added to the input of the converter), causes an unstable condition. Depending on the internal transformer's impedance, the external impedance usually should not exceed the 10% of the internal. So, the source impedance of the Power bus should be kept as low as possible.

The method to determine whether the impedance of the power bus too high or not is to decrease the converter's input voltage from higher to lower gradually, if the output voltage decreases (unstable sometime) with the lower input voltage, it will be considered the impedance too large. For further confirmation, one electrolytic capacitor can be paralleled to the converter pins after the converter shuts down (one $1\mu F$ ceramic capacitor may be required to be paralleled with the electrolytic capacitor), if the output getting better, it will be sure that the impedance is too large.

Page 4 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

Remote Control

Remote control can be offered by setting right control voltage level (or floating) to Cnt pin. ZDS80-28S5C is provided with positive logic remote control. When the level is higher than 3.5V or be left floating, the converter will be turned on. When the level is less than 1.5V, the converter will be turned off. When low voltage level is applied, the output current of the Cnt is less than 1mA. When high voltage level $(3.5V\sim15V)$ is applied, the input current of the Cnt is less than 1mA

Due to the logic comparator is semiconductor integrated chip, they have low endurance to surge. Care should be taken to prevent Cnt from surge, A TVS should be used in some cases. When the pin is left floating, 2.0V-6.0V voltage appears on lite pin.

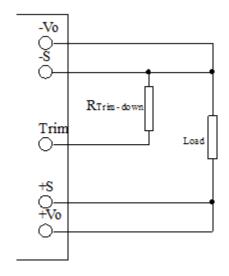
In some applications it is necessary to have a precise turn on and turn off level, or the level which can be received has a very narrow range, the aux. circuit will be required. Please contact us for more information.

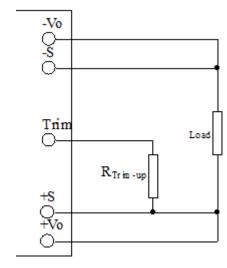
External Capacitance

Unless special purpose (i.e. prolonging hold-up time, input impedance matching), the recommended input filter's capacitance ranges $100\mu\text{F}$ - $220\mu\text{F}$, which not only offers a stable system, and reduces the cost, but also lessens the inrush current when the power supplies.

When larger capacitance is required, a circuit of suppressing the inrush current is recommended when the regulator start-up and a discharge circuit is recommended when the output dropped, ensuring the reliability and safety of other equipments in the system.

Output Voltage Adjust





Connection for Trimming Down

Connection of Trimming Up

The converters have an Output Voltage adjust pin (Trim). This pin can be used to adjust the output voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense offset) must be kept below the maximum output adjust range, or the characteristics will not be assured in compliant with the specification, even the over voltage protection may be triggered. Also note that at increased output voltages the maximum power rating of the converter 150W remains the same, and the output current capability will decrease correspondingly, at decrease output voltages the maximum current should not exceed 30A. When the trim pins are not used, they should be floated

External circuit is connected as the figure shown, the resistance is calculated as the formula below, please note that

Page 5 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

the formula will be invalid when $R_{Trim-up}$, $R_{Trim-down}$ are used simultaneously, users adjust the value based on the resistance applied,

$$R_{Trim-up} = \left(\frac{5.11 \times Vo(100(\%) + \Delta(\%))}{1.225 \times \Delta(\%)} - \frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22\right) (k \Omega)$$

Resistance for trimming up:

$$R_{Trim-down} = \left(\frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22\right) (k \Omega)$$

Resistance for trimming down:

Vo: rated output voltage, 5V;

$$R_{\it Trim-up}$$
, $R_{\it Trim-down}$: Resistance for trimming up or down, $k\Omega$;

 Δ (%): Change rate, divide output voltage by rated output voltage

Remote Sense

The remote sense can be used to compensate for the voltage drop between the output pins of the converter and the load input pins by +S, -S pins. The +S and -S pins should be connected to the input pins of the load respectively. The remote sense circuit will compensate for up to 10% voltage drop between the sense voltage and the voltage at the output pins. If the remote sense is not needed, the -S should be connected to +V out.

The anti-interference design should be considered when the $+S_x$ -S pins are connected to the pins to be compensated. The $+S_x$ -S traces should be located close to a ground trace or ground plane, and the area they surrounded should be minimized (just for electrical isolation); If cable connection presents, twisted pair wires should be used, EMI core are equipped with the twisted pair wires to reduce common mode noise when necessary, the sense leads should not be longer than 200mm,or the system characteristics may not be assured.

The sense leads only can carry very little current, and are not used for converter power output. Care should be taken in operation to avoid damaging the converter.

Over Temperature Protection

The converters provides over temperature protection function. When the PCB temperature (reference point A, see the figure on the right) exceeds the temperature trig point $(110~^{\circ}\text{C})$, the OTP circuit will cut down output power. The converter will stop until safe operating temperature is restored. Hysteresis temperature between OTP trig point and restart is approx 10°C . Time between OTP and restart is dependent on cooling of DC/DC converter.

Thermal Consideration

The converters operate in a variety of thermal environments; however, sufficient cooling should be provided to ensure reliable operation of the unit. Heat is removed by conduction, convection and radiation to the surrounding environment. For the specified ambient temperature, user can increase airflow and change the size of heatsink to improve the heat dissipating for the module with baseplate; user can only increase airflow to improve the heat dissipating for the models without heat sink. Note that the natural convection condition means that airflow is 0.1 m/s.

Output Over Voltage Protection

The converter is designed with clamped over voltage protection, when output voltage exceeds 120% to 140% of the rated output voltage (the set point is between 120% to 140%, there is the difference based on the specific parameters, but not beyond the range), the output voltage will be clamped. Be advised that to shut down the converter by using

Page 6 of 7 3/17/2023



16~40Vin/5V 16.5A out, Sixteen brick

remote control if it can not be repaired timely. Avoid the continuous resetting of the unit because that will damage the converter.

Safety Consideration

The converter, as a component for the end user, should be installed into the equipment, and all the safety considerations are achieved under certain condition. It is required to meet safety requirements in system design. The converter output is considered SELV, and the expected input is considered TNV2, the primary to secondary is basic insulation to EN60950. The maximum operating temperature for PCB is 170 °C.

To avoid fire and be protected when short circuit occurred, it is recommended that a fast blow fuse with rating 2.5 to 3 times of converter's continuous input peak current is used at the input terminal.

Series and Parallel Operation

The converters should not be paralleled directly to increase power, but they can be paralleled each other through o-ring switches or diodes. Make sure that every converter's maximum load current should not exceed the rated current at anytime if they are paralleled without using external current sharing circuits. For the case that there is no external current sharing circuit, but power needs to be increased, please use Trim pin to adjust each converter's output voltage, to load the current as equal as possible in operating (When the load changes in a wide range, the method will not work).

The converters can operate in series. To prevent against start-up failure due to start up time difference, SBD with low voltage difference can be paralleled at the output pins(SBD negative terminal connect to the positive pin of the output) for each converter.

ESD Control

The converters are processed and manufactured in an ESD controlled environment and supplied in conductive packaging to prevent ESD damage from occurring before or during shipping. It is essential that they are unpacked and handled using an ESD control procedures. Failure to do so affects the lifetime of the converter.

Quality Statement

The converters are manufactured in accordance with ISO 9001 system requirements, and are monitored 100% by auto-testing system, 100% burn in.

The warranty for the converters is 5-year.

Contact Information

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Page 7 of 7 3/17/2023