

TSR/ TISR Series

Voltage and current surge protection, DIP package

Features:

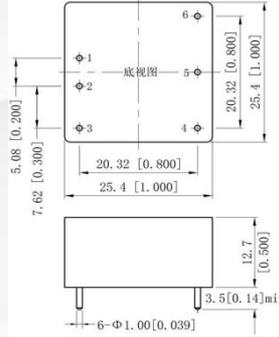
- ◆ Options include voltage suppression, current suppression
- ◆ Compact size, low power consumption, and high reliability
- ◆ Complies with GJB 298 and GJB 181B requirements for voltage surge protection
- ◆ Customized current suppression function



TSR Overvoltage and Undervoltage Surge Suppression Series

Performance parameters	Minimum	Typical	Maximum	Unit	Test Conditions
Working temperature	-45		+85	°C	
Storage Temperature	-55		+125	°C	
Overvoltage suppression	36	37	38	V	100V/50ms
Undervoltage suppression	16	20	21	V	6V/1s

Product model	Input voltage (Vdc)	Clamp voltage (Vdc)	Surge time (ms)	Maximum current (A)
TSR100S5-OU	6~100	37	50/100V	5



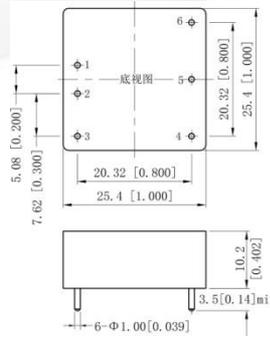
引脚	功能	说明
1	+Vin	输入正
2	COM	输入输出公共地
3	CNT	遥控
4	COM	输入输出公共地
5	NP	无引脚
6	+Vo	输出正

外壳材质: 铝壳, 环氧盖板;
 引脚材质: 黄铜, 表面镀金。
 单位: mm[inch]
 未注公差:
 X.X±0.5 X.XX±0.1
 引脚直径公差: ±0.1mm

TISR Surge Voltage and Current Suppression Series

Performance parameters	Minimum	Typical	Maximum	Unit	Test Conditions
Working temperature	-45		+85	°C	
Storage Temperature	-55		+125	°C	
Surge current suppression		2	2.2	A	Maximum power 50W
Surge voltage suppression		39	40	V	100V/50ms
Maximum capacitive load	0		1000	μF	Power less than 50W

Product model	Input voltage (Vdc)	Clamp voltage (Vdc)	Surge time (ms)	Maximum current (A)
TISR36S2AM	9~100	39	50/100V	2 (28V)
TISR75S2AM	16~100	80	100/100V	2 (48V)



引脚	功能	说明
1	+Vin	输入正
2	-Vin	输入负
3	CNT	遥控
4	-Vo	输出负
5	NP	无引脚
6	+Vo	输出正

输入负和输出负不能共地, 共地后冲击电流抑制 2A 失效;
 外壳材质: 铜壳, 环氧盖板;
 引脚材质: 黄铜, 表面镀金。
 单位: mm[inch]
 未注公差:
 X.X±0.5 X.XX±0.1
 引脚直径公差: ±0.1mm